

ABSTRACT OF THE DISCLOSURE

An array substrate for use in a liquid crystal display device includes a thin film transistor as a switching element, having a gate electrode, a source electrode and a drain electrode, wherein the gate electrode is a portion of a gate line near the crossing of the gate and data lines, and has an inverted "T"-shaped opening or a rectangularly-shaped opening. The drain electrode is shaped like the inverted "T"-shape and corresponds to the opening of the gate electrode. The source electrode surrounds the drain electrode along the steps of the semiconductor layer. Accordingly, in the thin film transistor having this structure, the gate electrode is only overlapped by the edges of the drain electrode. And thus, the gate-drain parasitic capacitance is reduced and minimized. Also, variations in the gate-drain parasitic capacitance are prevented. As a result, a high resolution is achieved and the picture quality is improved in the liquid crystal display device.